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# ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

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## Title of Invention

[METHOD OF FABRICATING A THIN FILM TRANSISTOR  
ARRAY PANELSUBSTRATE]

Application Number : 10/604 806

Confirmation Number:

First Named Applicant: Ko-Chin Yang

Attorney Docket Number: 10681-US-PA

Art Unit: 2813

Examiner: THOMPSON

Search string: ( 5407845 ).pn

## US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
CA	1	5407845	1995-04-18	Fujitsu Limited		437/40	H01L21265

## Signature

Examiner Name	Date
Craig A. Thompson	2/22/05